5   configuring the second interrupt controller to manage interrupts of at least the first interrupt

6   type.

1   2.   (Amended) The method of claim 1, further comprising:

2   configuring a system management interrupt to recognize initializing data related to at least

3   the first interrupt type.

1   3.   (Amended) The method of claim 1, further comprising:

2   configuring a system management interrupt to recognize initializing data related to at least

3   the first interrupt type; and

4   re-routing initializing data to the second interrupt controller starting from a first command

5   word,

6   wherein initializing data related to at least the first interrupt type comprises a plurality of

7   command words including the first command word that begins the initializing of the first interrupt

8   controller.

1   4.   (Amended) The method of claim 1, wherein the first interrupt controller comprises an

2   82C59 controller and the second interrupt controller comprises an advanced programmable

3   interrupt controller.

1   5.   A machine readable storage media containing executable program instructions which when

2   executed cause a digital processing system to perform a method comprising:

3   sending initializing data related to at least a first interrupt type to a first interrupt controller;

4   re-routing initializing data related to at least the first interrupt type to a second interrupt

5   controller; and

6   configuring the second interrupt controller to manage interrupts of the first interrupt type.

1   6.   The media of claim 5, further comprising:

2   configuring a system management interrupt to recognize initializing data related to at least

3   the first interrupt type.

1   7.   The media of claim 5, further comprising:

2   configuring a system management interrupt to recognize initializing data related to at least

3   the first interrupt type; and

4   re-routing initializing data to the second interrupt controller starting from a first command

5   word,

6   wherein initializing data related to at least the first interrupt type comprises a plurality of

7   command words including the first command word that begins the initializing of the first interrupt

8   controller.

1   8.   The media of claim 5, wherein the first interrupt controller comprises an 82C59 controller

2   and the second interrupt controller comprises an advanced programmable interrupt controller.

1   9.   A system comprising:

2   a central processing unit (CPU);

3   a first bus coupled to the CPU;

4   a first interrupt controller, coupled to the first bus, operable to manage communication with

5   the CPU of interrupts of a first interrupt type;

6   a second bus coupled to the CPU;

7   a second interrupt controller, coupled to the second bus and to the first interrupt controller,

8   operable to manage communication with the CPU of interrupts of a second interrupt type; and

9   a memory coupled to the second interrupt controller comprising a computer-readable

10 medium having a computer-readable program embodied therein for directing operation of the

11 system, the computer-readable program comprising:

12   instructions for managing interrupts of the first interrupt type by the second interrupt

13 controller, exclusive of the first interrupt controller.

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1   10.   The system of claim 9, wherein the computer-readable program further comprises:

2   instructions for sending initializing data related to at least a first interrupt type to the first

3   interrupt controller;

4   instructions for re-routing initializing data related to at least the first interrupt type to the

5   second interrupt controller; and

6   instructions for configuring the second interrupt controller to manage interrupts of the first

7   interrupt type.

1   11.   The system of claim 10, wherein the instructions for re-routing initializing data comprise:

2   instructions for configuring a system management interrupt to recognize initializing data

3   related to at least the first interrupt type.

1   12.   The system of claim 10, wherein initializing data related to at least the first interrupt type

2   comprise a plurality of command words and a first command word begins the initializing of the

3   first interrupt controller, and the computer-readable program comprises instructions for configuring

4   a system management interrupt to recognize initializing data related to at least the first interrupt type

5   and re-route initializing data to the second interrupt controller from the first command word.

1   13.   The system of claim 9, wherein the first interrupt controller comprises an 82C59 controller

2   and the second interrupt controller comprises an advanced programmable interrupt controller.

1   15.   A system comprising:

2   a central processing unit (CPU);

3   first means of interrupt processing for managing communication with the CPU of interrupts

4   of a first interrupt type;

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